

Call for Papers

The 16th International Workshop on Logic & Synthesis (IWLS)

May 30 – June 1, 2007

Paradise Point Resort & Spa
San Diego, CA

www.iwls.org

General Chair

Mukul Prasad
Fujitsu Labs of America

Program Chair

Victor Kravets
IBM T. J. Watson

Publicity Chair

Marc Riedel
University of Minnesota

Panel Chair

Sunil Khatri
Texas A&M University

Special Activities Chair

Alan Mishchenko
U.C. Berkeley

Programming Challenge

Christoph Albrecht
Cadence Berkeley Labs
& Florian Krohm, IBM

The International Workshop on Logic and Synthesis provides a research forum for the exchange of ideas pertaining to all aspects of synthesis, optimization, and verification of integrated circuits and systems. The emphasis is on novelty and intellectual rigor. The workshop encourages early dissemination of ideas and results. Accepted papers are distributed only to IWLS participants.

Topics of interest include (but are not limited to): synthesis and optimization; power and timing analysis; testing and verification; architectures and compilation; and design experiences. Submissions on modeling, analysis and synthesis for **emerging technologies** and **platforms** are particularly encouraged.

Authors may submit complete papers; these must be no longer than 8 pages, double column, 10-point font. They may also submit extended abstracts, highlighting important new problems in the early stages of development, without providing complete solutions. Submissions are made electronically through the website: <http://www.iwls.org>. For questions, contact IWLS_pcchair@sigda.org. For travel grants, apply to ACM/SIGDA's travel grant program at <http://www.sigda.org/travelgrants.html>.

The workshop format includes paper presentations, posters, invited talks, social lunch and dinner gatherings, and recreational activities.

For the second time, the workshop includes a programming challenge for students, sponsored by IEEE CEDA. Students compete in the implementation of logic optimization algorithms with the OpenAccess Gear infrastructure. Travel grants and cash prizes are awarded for significant contributions. The winning submissions from last year are now part of the OA Gear release. For more information, see <http://www.iwls.org/challenge/>

Sponsored by IEEE and
ACM/SIGDA.



Submission deadline for papers (<i>extended</i>)	March 16, 2007 (<i>Midnight EST</i>)
Notification of acceptance	April 13, 2007
Final version due	April 23, 2007

Technical Program Committee

C. Albrecht, Cadence Berkeley Labs	Y. Kukimoto, Extreme DA
R. I. Bahar, Brown University	I. Markov, University of Michigan
M. Berkelaar, Magma Design Automation	A. Mishchenko, University of California, Berkeley
V. Bertacco, University of Michigan	R. Murgai, Fujitsu Labs of America
R. Brayton, University of California, Berkeley	S. Nowick, Columbia University
E. Dubrova, KTH, Sweden	M. Prasad, Fujitsu Labs of America
S. Edwards, Columbia University	M. Riedel, University of Minnesota
M. Hutton, Altera	H. Savoj
E. Jacobs, Magma Design Automation	S. Sinha, Synopsys
T. Kam, Intel	C. Stangier, Mentor Graphics
S. Khatri, Texas A&M University	P. Stanley-Marbell, TU Eindhoven
V. Kravets, IBM T. J. Watson	M. Theobald, D.E. Shaw
P. Kudva, IBM T. J. Watson	T. Villa, Universita di Udine/PARADES, Italy
A. Kuehlmann, Cadence Berkeley Labs	H. Zhou, Northwestern University