

# Call for Papers

## The 30th International Workshop on Logic & Synthesis (IWLS)

July 19 – 21, 2021, Virtual Conference  
[www.iwls.org](http://www.iwls.org)

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The International Workshop on Logic and Synthesis is the premier forum for research in synthesis, optimization, and verification of integrated circuits and systems. Research on logic synthesis for emerging technologies and for novel computing platforms, such as nanoscale systems and biological systems, is also strongly encouraged. The workshop encourages the early dissemination of ideas and results. The workshop accepts complete papers highlighting important new problems in the early stages of development, without providing complete solutions. The emphasis is on novelty and intellectual rigor. Topics of interest include but are not limited to: hardware synthesis and optimization; software synthesis; hardware/software co-synthesis; power and timing analysis; testing, validation, and verification; synthesis for reconfigurable architectures; hardware compilation for domain-specific languages; and design experiences. Submissions on modeling, analysis, and synthesis for emerging technologies and platforms are particularly encouraged.

**Call for regular papers:** Only complete papers with original and previously unpublished material are permitted. Submissions must be no longer than 8 pages, double column, 10-point font. Accepted papers are distributed only to IWLS participants. The workshop format includes paper presentations, posters, invited talks, social lunch and dinner gatherings, and recreational activities. Submissions are made electronically through [EasyChair](https://www.easychair.org). Please see the workshop website for instructions: [www.iwls.org](http://www.iwls.org).

Paper abstract submission ~~April 19, 2021~~ **May 3, 2021**

Submission deadline for papers ~~April 26, 2021~~ **May 3, 2021**

Notification of acceptance June 18, 2021

Final version due June 30, 2021

Submission link: <https://easychair.org/conferences/?conf=iwls2021>

**IWLS 2021 Programming Contest Machine Learning + Logic Synthesis:** We are announcing the second ML+LS contest this year. The contest this year extends, in a fairly significant way, the contest from last year. The IWLS'20 contest generated a large number of interesting ideas and approaches that are summarized in an invited [DATE 21 paper](#). This year, we extend it to the multi-output case, that is, to the case of learning a function. Since this is much more challenging than the task last year, instead of 100 separate benchmarks, we will use only one benchmark dataset [CIFAR-10](#). More details can be found in the [Call for Contest](#).

### Technical Program Committee (Tentative)

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