

## Call for Papers

# The 31st International Workshop on Logic & Synthesis (IWLS)

July 18 – 21, 2022

Virtual Conference

[www.iwls.org](http://www.iwls.org)

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Luca Amaru  
Synopsys, USA

### **Program Committee Chairs**

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Synopsys, USA / Univ. of Milan, IT

### **Program Contest Chair**

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UC Berkeley, USA / Google, USA

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Synopsys, IT

The International Workshop on Logic and Synthesis is the premier forum for research in synthesis, optimization, and verification of integrated circuits and systems. Research on logic synthesis for emerging technologies and for novel computing platforms, such as nanoscale systems and biological systems, is also strongly encouraged. The workshop encourages early dissemination of ideas and results. The workshop accepts complete papers highlighting important new problems in the early stages of development, without providing complete solutions. The emphasis is on novelty and intellectual rigor.

Topics of interest include, but are not limited to: hardware synthesis and optimization; software synthesis; hardware/software co-synthesis; power and timing analysis; testing, validation and verification; synthesis for reconfigurable architectures; hardware compilation for domain-specific languages; and design experiences. Submissions on modeling, analysis and synthesis for emerging technologies and platforms are particularly encouraged.

**Call for regular papers:** Only complete papers with original and previously unpublished material are permitted. Submissions must be no longer than 8 pages, double column, 10-point font. Accepted papers are distributed only to IWLS participants. The workshop format includes paper presentations, posters, invited talks and social coffee breaks. Submissions are made electronically through [EasyChair](#). Please see the workshop [website](#) for instructions.

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#### Important Dates

Paper abstract submission	April 11, 2022
Submission deadline for papers	April 18, 2022 @ 11.59pm Anywhere on Earth
Notification of acceptance	June 24, 2022
Final version due	July 06, 2022

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#### IWLS 2022 Programming Contest

The goal of this year's contest is to synthesize small circuits for completely-specified multi-output Boolean functions represented using truth tables. Participants should find competitive solutions for different benchmarks using a variety of novel break-through methods (e.g., search and enumeration, new decomposition, etc.).

To participate, check out the [contest description](#) and download the [benchmark](#). More details on the submission rules and deadlines will be available soon.

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#### Technical Program Committee

Luca Amaru, Synopsys  
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Zhufei Chu, Ningbo University  
Stephan Eggersglüß, Siemens EDA  
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