

Call for Papers

The 30th International Workshop on Logic & Synthesis (IWLS)

July 19 – 21, 2021

Virtual Conference

www.iwls.org

General Chair

Vinicius Callegaro, Siemens EDA

Program Chairs

Cunxi Yu, University of Utah

Gai Liu, Xilinx

Special Session Chair

Luca Amaru, Synopsys

Programming Contest Chairs

Satrajit Chatterjee, Google

Alan Mishchenko, University of
California Berkeley

Proceedings Chair

Zhufei Chu, Ningbo University

Finance Chair

Jody Matos, Silvaco

Publicity Chair

Eleonora Testa, Synopsys

Virtual Chairs

Augusto Neutzling, Cadence

Walter Lau Neto, University of Utah

The International Workshop on Logic and Synthesis is the premier forum for research in synthesis, optimization, and verification of integrated circuits and systems. Research on logic synthesis for emerging technologies and for novel computing platforms, such as nanoscale systems and biological systems, is also strongly encouraged. The workshop encourages the early dissemination of ideas and results. The workshop accepts complete papers highlighting important new problems in the early stages of development, without providing complete solutions. The emphasis is on novelty and intellectual rigor.

Topics of interest include but are not limited to: hardware synthesis and optimization; software synthesis; hardware/software co-synthesis; power and timing analysis; testing, validation, and verification; synthesis for reconfigurable architectures; hardware compilation for domain-specific languages; and design experiences. Submissions on modeling, analysis, and synthesis for emerging technologies and platforms are particularly encouraged.

Only complete papers with original and previously unpublished material are permitted. Submissions must be no longer than 8 pages, double column, 10-point font. Accepted papers are distributed only to IWLS participants. The workshop format includes paper presentations, posters, invited talks, social lunch and dinner gatherings, and recreational activities. Submissions are made electronically through [EasyChair](https://www.easychair.org). Please see the workshop website for instructions: www.iwls.org.

Paper abstract submission April 19, 2021

Submission deadline for papers April 26, 2021

Notification of acceptance June 18, 2021

Final version due June 30, 2021

Submission link: <https://easychair.org/conferences/?conf=iwls2021>

Technical Program Committee (Tentative)

Luca Amaru, Synopsys

Vinicius Callegaro, Siemens EDA

Petr Fiser, CTU

Tsung-Yi Ho, NTHU

Jie-Hong Roland Jiang, National Taiwan University

Gai Liu, Xilinx Inc

Jody Matos, Silvaco

Augusto Neutzling, Cadence

Weikang Qian, Shanghai Jiao Tong University

Heinz Riener, EPFL

Mathias Soeken, Microsoft

Tiziano Villa, Universita' di Verona

Cunxi Yu, University of Utah

Zhufei Chu, Ningbo University

Pierre-Emmanuel Gaillardon, University of Utah

Tsung-Wei Huang, University of Utah

Victor Kravets, IBM

Felipe Marranghello, Synopsys

Alan Mishchenko, UC Berkeley

Elie El Aaraj, Cadence

Andre Inacio Reis, UFRGS

Tsutomu Sasao, Meiji University

Eleonora Testa, Synopsys

Robert Wille, Johannes Kepler University

Yuan Zhou, Cornell University/AWS

