

# Call for Papers

## The 32nd International Workshop on Logic & Synthesis (IWLS)

June 5-6, 2023, EPFL, Switzerland  
[www.iwls.org](http://www.iwls.org)

### General Chair

Cunxi Yu, University of Utah

### Program Committee Chairs

Xiaoqing Xu, X, the moonshot factory

Lana Josipović, ETH Zurich

### Special Session Chair

Eleonora Testa, Synopsys

### Programming Contest Chair

Alan Mishchenko, University of  
California Berkeley

### Proceedings Chair

Walter Lau Neto, University of Utah

### Finance Chair

Zhufei Chu, Ningbo University

### Publicity Chairs

Augusto Neutzling, Cadence

Yingjie Li, University of Utah

### Local Arrangements Chair

Siang-Yun Lee, EPFL



The International Workshop on Logic and Synthesis is the premier forum for research in synthesis, optimization, and verification of integrated circuits and systems. Research on logic synthesis for emerging technologies and for novel computing platforms, such as nanoscale systems and biological systems, is also strongly encouraged. The workshop encourages the early dissemination of ideas and results. The workshop accepts complete papers highlighting important new problems in the early stages of development, without providing complete solutions. The emphasis is on novelty and intellectual rigor.

Topics of interest include but are not limited to: hardware synthesis and optimization; software synthesis; hardware/software co-synthesis; power and timing analysis; testing, validation, and verification; synthesis for reconfigurable architectures; hardware compilation for domain-specific languages; and design experiences. Submissions on modeling, analysis, and synthesis for emerging technologies and platforms are particularly encouraged.

The workshop format includes paper presentations, posters, invited talks, social lunch and dinner gatherings, and recreational activities.

**Call for regular papers:** Only complete papers with original and previously unpublished material are permitted. Submissions must be no longer than 8 pages, double column, 10-point font. Accepted papers are distributed only to IWLS participants. Submissions are made electronically through [EasyChair](https://www.easychair.org). Please see the workshop website for instructions: [www.iwls.org](http://www.iwls.org).

Paper abstract submission: April 3, 2023

Submission deadline for papers: April 10, 2023

Notification of acceptance: May 15, 2023

Final version due: June 02, 2023

Submission link: <https://easychair.org/conferences/?conf=iwls2023>

**IWLS 2023 Programming Contest:** Submission instructions and contest problems will be released soon.



### Technical Program Committee (Tentative)

Luca Amaru, Synopsys	Giulia Meuli, Synopsys
Anna Bernasconi, Università di Pisa	Alan Mishchenko, UC Berkeley
Vinicius Callegaro, Siemens EDA	Augusto Neutzling, Cadence Design System
Sat Chatterjee, Google	Weikang Qian, Shanghai Jiao Tong University
Zhufei Chu, Ningbo University	Andre Reis, UFRGS
Valentina Ciriani, Università degli Studi di Milano	Heinz Riener, Cadence Design System
Stephan Eggersgluß, Siemens EDA	Tsutomu Sasao, Meiji University
Petr Fišer, CTU	Herman Schmit, Google
Jie-Hong Roland Jiang, National Taiwan University	Mathias Soeken, Microsoft
Lana Josipović, ETH Zurich	Eleonora Testa, Synopsys
Victor Kravets, IBM	Tiziano Villa, Università degli Studi di Verona
Siang-Yun Lee, EPFL	Robert Wille, Johannes Kepler University
Yingjie Li, University of Utah	Xiaoqing Xu, X, the moonshot factory
Walter Lau Neto, University of Utah	Cunxi Yu, University of Utah