Call for Papers

The 35th International Workshop on Logic & Synthesis (IWLS 2026)

May 29-31, 2026, Hong Kong, China

https://www.iwls.org

General Chair

Zhufei Chu, *Ningbo University*

Program Committee Chairs

Walter Lau Neto, *Synopsys*

Weikang Qian, *Shanghai Jiao Tong Univ.*

Programming Contest Chairs

Alan Mishchenko, *UC Berkeley*

Attila Jurecska, *Siemens EDA*

Special Session Chair

Tsung-Yi Ho, *Chinese University of Hong Kong*

Finance Chair

Fengbin Tu, *The Hong Kong University of Science and Technology*

Proceedings Chair

Guojie Luo, *Peking University*

Publicity Chairs

Cunxi Yu, *University of Maryland*

Marcel Walter, *Technical University of Munich*

Local Arrangements Chairs

Rongliang Fu, *Chinese University of Hong Kong*

Zhenxuan Xie, *Chinese University of Hong Kong*

The International Workshop on Logic and Synthesis is the premier forum for research in synthesis, optimization, and verification of integrated circuits and systems. Research on logic synthesis for emerging technologies and for novel computing platforms, such as nanoscale systems and biological systems, is also strongly encouraged. The workshop encourages the early dissemination of ideas and results. The workshop accepts complete papers highlighting important new problems in the early stages of development, without providing complete solutions. The emphasis is on novelty and intellectual rigor.

Topics of interest include but are not limited to: hardware synthesis and optimization; software synthesis; hardware/software co-synthesis; power and timing analysis; testing, validation, and verification; synthesis for reconfigurable architectures; hardware compilation for domain-specific languages; and design experiences. Submissions on modeling, analysis, and synthesis for emerging technologies and platforms are particularly encouraged.

The workshop format includes paper presentations, invited talks, social lunch and dinner gatherings, and recreational activities.

Call for regular papers: Only complete papers with original and previously unpublished material are permitted. Submissions must be no longer than 8 pages (references excluded), double column, 10-point font (we recommend using the ACM template or the IEEE template, but not necessarily). Accepted papers are distributed only to IWLS participants.

Double-blind policy: IWLS uses a double-blind reviewing system. Manuscripts must not identify authors or their affiliations; those that do will not be considered.

If you have questions about how to meet these guidelines, please contact the program chairs before the submission deadline.

The papers should be submitted to Easychair: IWLS 2026 submission page.

Paper abstract submission: March 20, 2026 (AoE)
Submission deadline for papers: March 27, 2026 (AoE)
Notification of acceptance: May 3, 2026 (AoE)
Final version due: May 20, 2026 (AoE)

Technical Program Committee

Luca Amarù, *Synopsys*
Anna Bernasconi, *University of Pisa*
Lei Chen, *Huawei Noah's Ark Lab*
Zhufei Chu, *Ningbo University*
Fabrizio Ferrandi, *Politecnico di Milano*
Petr Fišer, *Czech Technical University in Prague*
Rongliang Fu, *The Chinese University of Hong Kong*
Aman Gayasen, *AMD*
Winston Haaswijk, *Cadence Design Systems*
Jie-Hong Roland Jiang, *National Taiwan University*
Attila Jurecska, *Siemens EDA*
Victor Kravets, *IBM*
Walter Lau Neto, *Synopsys*
Siang-Yun Lee, *Cadence Design Systems*
Guojie Luo, *Peking University*
Chang Meng, *Eindhoven University of Technology*
Giulia Meuli, *Synopsys*
Shin-ichi Minato, *Kyoto University*
Alan Mishchenko, *UC Berkeley*
Augusto Neutzling, *Real Intent*
Stefan Nikolic, *University of Novi Sad*
Weikang Qian, *Shanghai Jiao Tong University*
Stefano Quer, *Politecnico di Torino*
Andre Reis, *UFRGS*
Tsutomu Sasao, *Meiji University Herman*
Herman Schmit, *Google*
Eleonora Testa, *Synopsys*
Alessandro Tempia Calvino, *Synopsys*
Gabriella Trucco, *University of Milan*
Tiziano Villa, *University of Verona*
Marcel Walter, *Technical University of Munich*
Robert Wille, *TU Munich & SCCH GmbH*
Cunxi Yu, *University of Maryland*
Mingfei Yu, *EPFL*